







A High-Performance Stochastic Simulated Bifurcation Ising Machine

Tingting Zhang, Hongqiao Zhang, Zhengkun Yu, Siting Liu*, Jie Han













Outline

- Background
 - Ising machine
 - (Dynamic) stochastic computing
- Formulation and circuit design
- System design
- Experiments and results
- Conclusion

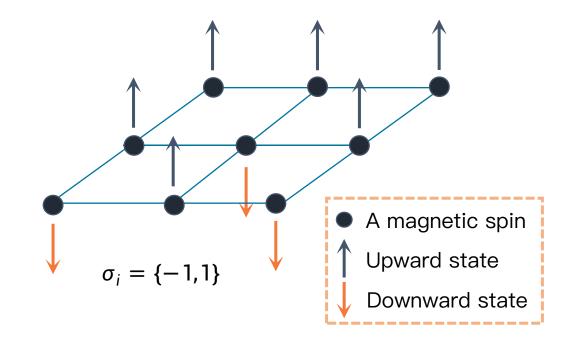


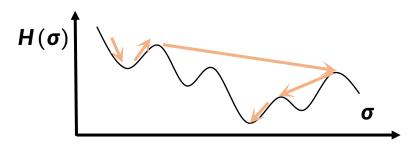
The Ising Model

- Describe ferromagnetic interactions of magnetic spins
- Each spin: either an upward (+1) or downward (-1) state
- Energy of an Ising model (Hamiltonian):

$$H(\sigma) = -\sum_{i,j} J_{ij} \sigma_i \sigma_j - \sum_i h_i \sigma_i$$

- Converge to the lowest energy state
- The Ising machine: Combinatorial optimization with a polynomial time





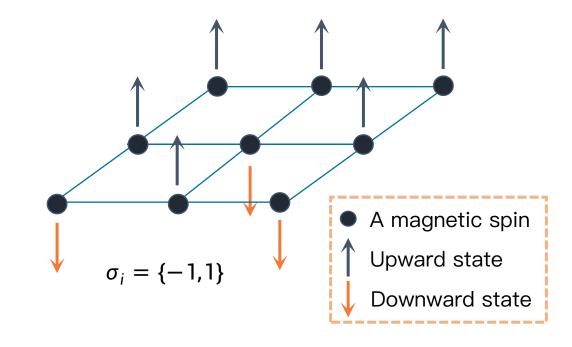


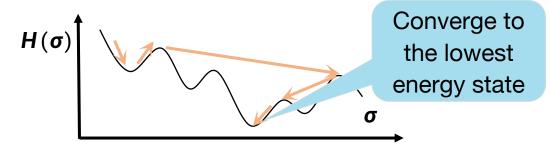
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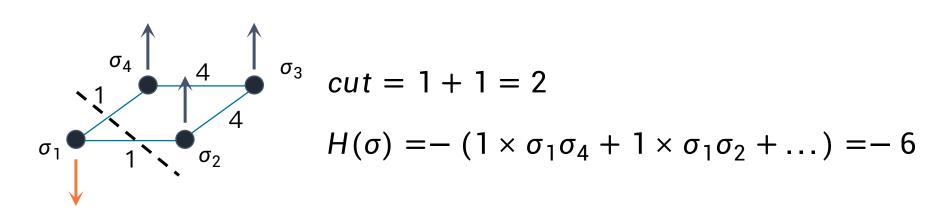






• The Max-cut problem (MCP): Partition the vertices in a weighted graph to two independent subsets such that the sum of edges between the subsets is maximized.

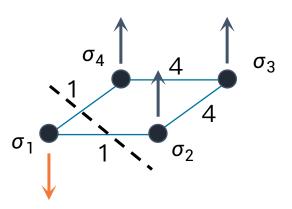
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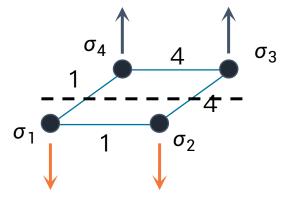


 The Max-cut problem (MCP): Partition the vertices in a weighted graph to two independent subsets such that the sum of edges between the subsets is maximized.

$$H(\sigma) = -\sum_{i,j} J_{ij} \sigma_i \sigma_j - \sum_i h_i \sigma_i$$



$$cut = 2$$
$$H(\sigma) = -6$$



$$cut = 1 + 4 = 5$$

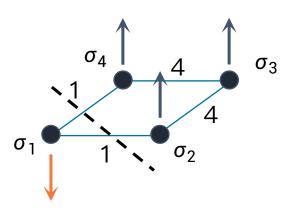
$$H(\sigma) = -(1 \times \sigma_1 \sigma_4 + 4 \times \sigma_2 \sigma_3 + \dots)$$

$$= 0$$

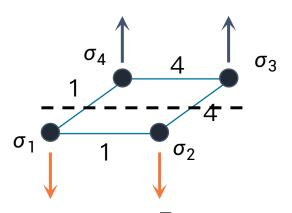


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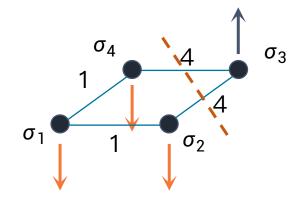
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$$cut = 2$$
$$H(\sigma) = -6$$



$$cut = 5$$
$$H(\sigma) = 0$$

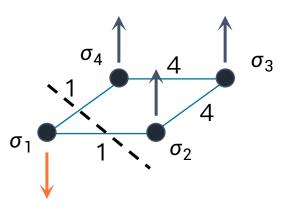


$$H(\sigma) = -(4 \times \sigma_3 \sigma_4 + \dots) = 6$$

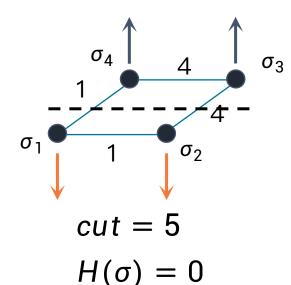


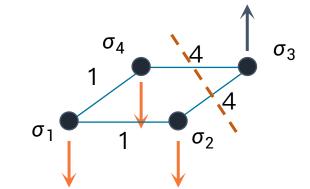
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$$H(\sigma) = -\sum_{i,j} J_{ij} \sigma_i \sigma_j - \sum_i h_i \sigma_i$$



$$cut = 2$$
$$H(\sigma) = -6$$





$$H(\sigma) = 6$$



• Good news: simulated bifurcation (SB) realizes parallel update of the spin values, unlike simulated annealing (SA).

Simulated bifurcation (SB)

$$\dot{x_{i,t}} = a_0 y_{i,t},$$

 $\dot{y_{i,t}} = -\{a_0 - a(t)\}x_{i,t} + c_0 J x_{i,t} + \eta(t) h_i$

 x_i is replaced with its sign and y_i is initialized to 0 if $|x_i| > 1$.

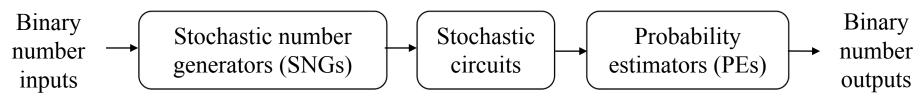
 $x_{i,t}$ and $y_{i,t}$ are the position and momentum of oscillator s_i , respectively. $J_{i,j}$ describes the interaction between s_i and s_j . a_0 and c_0 are constants. a(t) is a linear function. $x_{i,t}$ and $y_{i,t}$ are derivatives of $x_{i,t}$ and $y_{i,t}$, respectively.

 Bad news: solving differential equations is not easy, expecially when the matrices are large (compute-intensive)

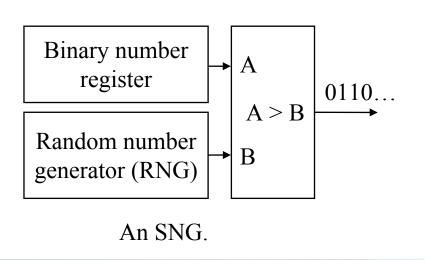


Stochastic Computing (SC)

 Good news: In SC, values are represented and processed as random bit streams of 0s and 1s; simple logic gates/counters can perform arithmetics

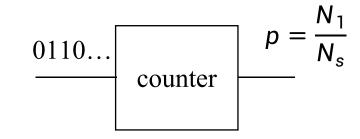


A stochastic computing system.



$$p_1 = 0.5$$
:
 $0110...$
 $p_2 = 0.5$:
 $p_1p_2 = 0.25$:
 $0100...$

A unipolar stochastic multiplier.



A probability estimator.

 N_1 : the number of 1s.

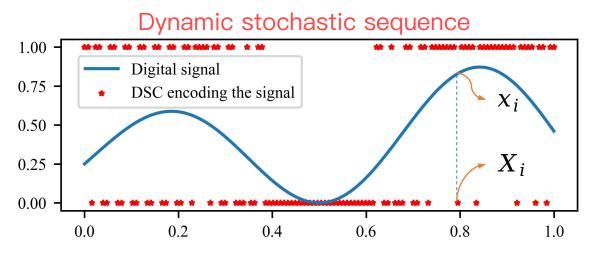
 N_s : the number of all bits.



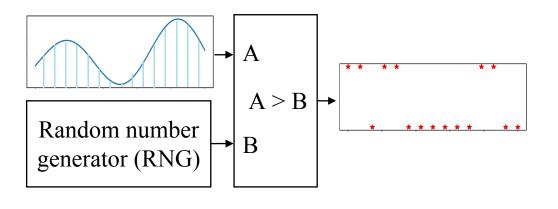
Dynamic Stochastic Computing (DSC)

 Good news: In DSC, signals are sampled as random bit streams of 0s and 1s; each bit encodes a (changing) value or probability of the signal.

Specifically, we use dynamic stochastic computing (DSC)



For each sampling point, $\mathbb{E}[X_i] = x_i$



A DSNG.



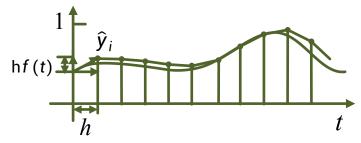
Dynamic Stochastic Computing (DSC, Cont'd)

 Good news: In DSC, signals are sampled as random bit streams of 0s and 1s; each bit encodes a (changing) value or probability of the signal.

Ordinary differential equation (ODE)

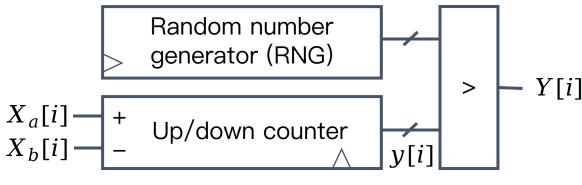
$$\frac{\mathrm{d}y(t)}{\mathrm{d}t}=f(t)$$

Euler method $\hat{y}_i \approx \sum_i f_i$



 $\hat{y}_i \approx \sum F_i F_i$: DSS encoding f(t) $y[i] = y(t)|_{t=hi} \approx \int [x_a(t) - x_a(t)]$ Instead

In our previous work, published in DAC'17 [1]



A stochastic integrator.

$$y[i] = y(t)|_{t=hi} \approx \int [x_a(t) - x_a(t)]$$



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Formulation of SB

$$x_{i,t} = a_0 y_{i,t} = f(\mathbf{y}_t)_i,$$

$$y_{i,t} = -\{a_0 - a(t)\}x_{i,t} + c_0 J \mathbf{x}_{i,t} + \eta(t) \mathbf{h}_i = g(\mathbf{x}_t)_i$$
A linear function Semi-implicit Euler integration

$$y_{i,t+1} = y_{i,t} + \eta g(\mathbf{x}_t)_i$$

$$x_{i,t+1} = x_{i,t} + \eta f(\mathbf{y}_{t+1})_i$$

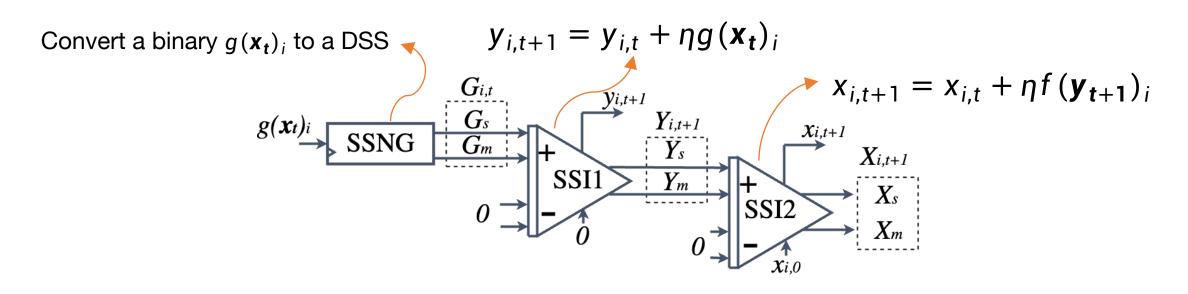


$$x_{i,t+1} = x_{i,0} + \eta^2 \sum_{j=0}^t \sum_{k=0}^j g(x_k)_i$$



A Stochastic Computing SB Cell

$$x_{i,t+1} = x_{i,0} + \eta^2 \sum_{j=0}^t \sum_{k=0}^j g(x_k)_i$$

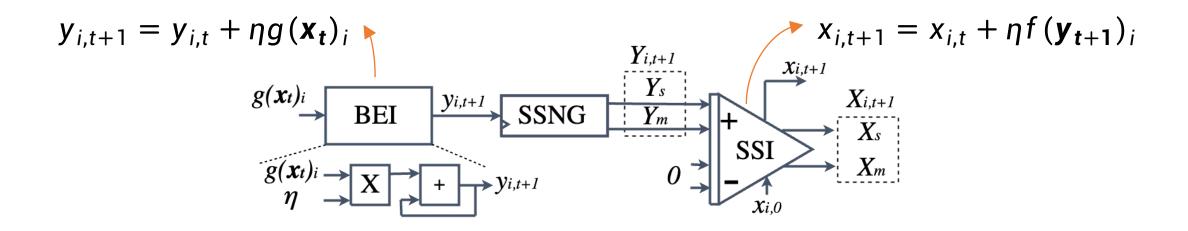


The Stochastic Computing SB Cells (SC-SBCs)
Aimed for higher area efficiency



A Binary-Stochastic Computing SB Cell

$$x_{i,t+1} = x_{i,0} + \eta^2 \sum_{j=0}^t \sum_{k=0}^j g(x_k)_i$$



The Binary-Stochastic Computing SB Cells (BSC-SBCs)
Aimed for higher speed

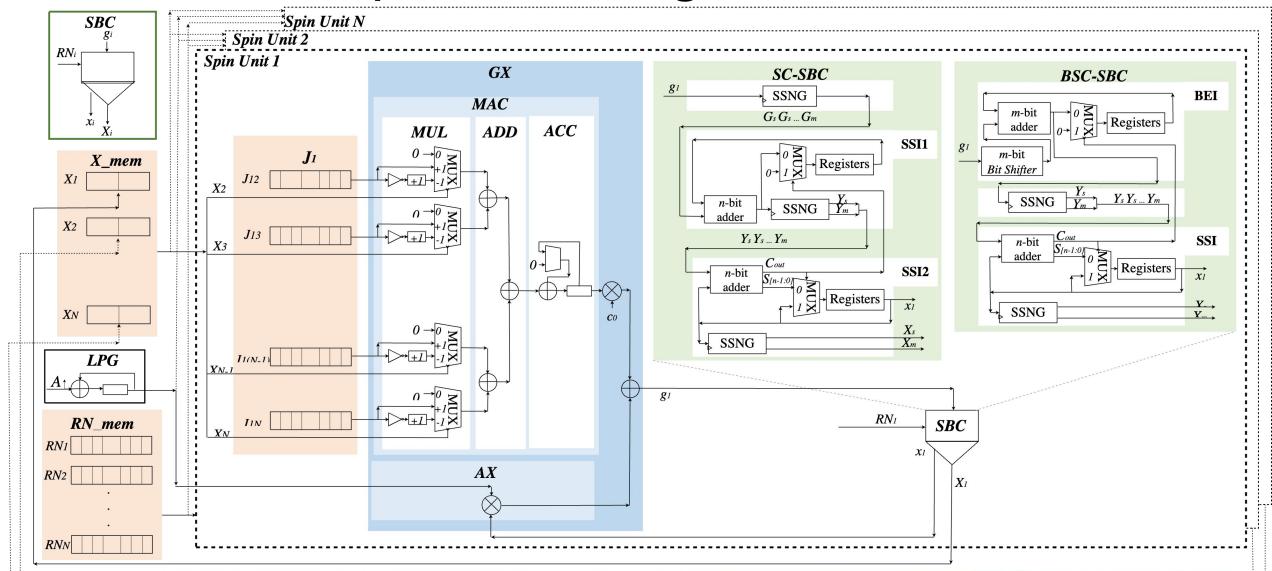


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The SSBM System Design



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Application: Max-Cut Problems (MCPs)

Experimental Setup

- Algorithms: bSB, dSB, SC-SBM (η = 0.125,0.25,0.5), BSC-SBM (η = 0.125,0.25,0.5).
- Benchmark: the *K2000* benchmark
- Time steps: $T_s = 1000$, $T_s = 10000$

■ Evaluation:

The statistics of cut values from 100 trails

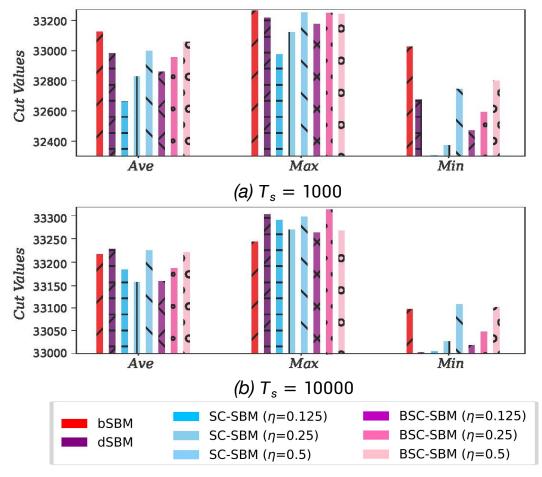
Ave: the average of cut values; **Max**: the maximum of cut values; **Min**: the minimum of cut values. A larger **Ave**, **Max** and **Min** indicate a higher performance, given by a higher likelihood to jump out of the local optima, and thus a higher stability.

• Probability-to-target (P_q) and Step-to-target (S_q)



Performance Evaluation

- The proposed SSBM: the higher *Ave* and *Min* values are obtained with η = 0.5 than with η = 0.125, 0.25.
- Evaluated by *Ave* and *Min*, when $\eta = 0.5$, the BSC-SBM performs better than the SC-SBM when $T_s = 1000$; the SC-SBM performs better than the BSC-SBM when $T_s = 10000$.
- It shows the advantages of BSC-SBM in a short search, and SC-SBM in a long search.



^{*} bSBM: ballistic simulated bifurcation machine; dSBM: discrete simulated bifurcation machine.



Performance Evaluation (Cont'd)

- For $T_s = 1000$, the SSBMs can achieve a higher $P_{99.5\%}$ value than dSBM. Moreover, the proposed BSC-SBM performs similarly to bSBM.
- For $T_s = 10000$, it is difficult for bSBM to reach $P_{99.8\%}$ of the best-known cut value due to the lack of ability to jump out of the local minima, and a better solution can be obtained by dSBM and SSBMs.
- It shows that SSBMs find a better solution than dSBM in a short search and have a lower probability of being stuck at the local minima than bSBM in a long search.

The Values of Pg and Sg for the Max-cut Problems on K2000 Benchmark

Vaules of P_g		SB Machines					
and S_g with T_s		bSBM	dSBM	SC-SBM	BSC-SBM		
$T_S = 1000$	$P_{99.5\%}$	38%	4%	6%	22%		
	$S_{99.5\%}$	7633	112811	74426	18534		
$T_S = 10000$	$P_{99.8\%}$	0	6%	4%	2%		
	$S_{99.8\%}$		744265	1128110	2279481		

^{*} K2000: 2000 nodes, 1999000 edges, a complete graph, edge weight $w_{ij} \in \{-1, +1\}$, best-known cut value: 33337.



Hardware Efficiency

■ Experimental Setup

- Ising Machines: D-wave[3], JSSC'21[8], JSSC'15[14], ISSCC'21[15], CICC'21[16], JSSC'22[17], vs.
 SC-SBM, BSC-SBM
- Simulation results for SC-SBM and BSC-SBM are obtained by using the Synopsys Design
 Compiler. A CMOS 40nm technology is applied with a supply voltage of 1.0 V and a temperature
 of 25℃.

■ Evaluation

- Computing Method; Technology; # Spin; Topology; # Spin Interactions; Coefficient Bit-Width;
 Spin Type
- Power per Spin; Area per Spin; Frequency; # Spin Update Cycles
- Normalized Power per Spin, Normalized Area per Spin



Hardware Efficiency (Cont'd)

- The dense connectivity between spins leads to an increase in area and power.
- The spins in SC-SBM and BSC-SBM require 1.5X and 1.3X more normalized power per spin than [8], respectively, due to the 3.9X larger connectivity.
- The proposed SC-SBM and BSC-SBM utilize at least 10.62% smaller normalized area than [8].

	D-wave [3]	JSSC'15 [14]	JSSC'21 [8]	ISSCC'21 [15]	CICC'21 [16]	JSSC'22 [17]	Prop. SC-SBM	Prop. BSC-SBM
Computing	Quantum	CMOS	SCA	Metropolis	Simulated	Simulated	Simulated	Simulated
Method	Annealing	Annealing	Annealing	Annealing	Annealing	Annealing	Bifurcation	Bifurcation
Technology	Superconductor	65nm CMOS	65nm CMOS	65nm CMOS	65nm CMOS	65nm CMOS	40nm CMOS	40nm CMOS
# Spins	2k	20 <i>k</i>	512	16 <i>k</i>	252	480	2k	2k
Topology	Chimera	Lattice	Complete	King	King	King	Complete	Complete
# Spin Interactions	5	5	511	8	8	8	1999	1999
Coefficient Bit-Width	N/A	2	5	5	4	4	2	2
Spin Type	Qubit	SRAM	SRAM	Register	Register	Register	Register	Register
Power per Spin	12.2 W	$2.83~\mu W$	1.27 mW	N/A	$1.33~\mu W$	$0.18~\mu W$	0.74~mW	0.64 mW
Area per Spin	N/A	289 μm^2	12207 μm^2	$552 \ \mu m^2$	1671 μm^2	832 μm^2	6370 μm^2	6453 μm^2
(Normalized Area)		(6.86 ×)	(1.13 ×)	(3.28 ×)	(12.41 ×)	$(6.17 \times)$	(1 ×)	(1.01 ×)
Frequency	N/A	100 MHz	320 MHz	100 MHz	64 <i>MHz</i>	200 MHz	250 <i>MHz</i>	250 MHz
# Spin Update Cycles	N/A	N/A	512	22	N/A	1	20	20



Conclusion

- A high-performance fully connected stochastic SB machine (SSBM) is designed for low-cost and accurate combinatorial optimization using the Ising model.
- Based on stochastic computing, two efficient SB cells are further designed by using SSIs to solve pairs of differential equations in SB.
- The 2000–spin fully connected SSBM using the SC–SBC or BSC–SBC as a building block realizes fast energy convergence in a short search and also prevents from being stuck at the local minimum in a long search.
- An improvement of at least 44% in power is achieved with a 1.19X speedup, compared to conventional SB machines.



References

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Q&A

Thanks for your attention!

